

ABSTRACT OF THE DISCLOSURE

The image display device has a display section formed of plural pixels and a control section which controls the display section, and is provided with nonvolatile phase-change  
5 type pixel memories in respective ones of the pixels, or is provided with a nonvolatile phase-change type frame memory in the control section. Each of the nonvolatile phase-change type pixel memories is formed of one or more switches and a  
variable-resistance memory element fabricated from a  
10 chalcogenide material for storing display data for at least a specified period of time. The nonvolatile phase-change type frame memory is formed of one or more switches and a  
variable-resistance memory element fabricated from a  
chalcogenide material for retaining display data for one frame.